U.S. **UTILITY** Patent Application

O.I.P.E.

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SCANNED _TM³_   Q.A. _LR_

APPLICATION NO. 09/655596	CONT/PRIOR	CLASS 703 717	SUBCLASS 138	ART UNIT 2123 2122	EXAMINER _Morse_ _T. Vu_

APPLICANTS

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TITLE

High speed software driven emulator comprised of a plurality of
emulation processors with a method to allow memory read/writes
without interrupting the emulation

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)				
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONAL CLASSIFICATION						
					☐ Continued on Issue Slip Inside File Jacket	

TERMINAL ☐ DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.

	DRAWINGS		

☐ The term of this patent
subsequent to _____ (date)
has been disclaimed.

(Assistant Examiner)   (Date)

NOTICE OF ALLOWANCE MAILED

☐ The term of this patent shall
not extend beyond the expiration date
of U.S Patent. No. _____

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ISSUE FEE

Amount Due	Date Paid

(Primary Examiner)   (Date)

ISSUE BATCH NUMBER

☐ The terminal ____months of
this patent have been disclaimed.

(Legal Instruments Examiner)   (Date)

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